

PCD1.G2000-A20

E-Line S-Serie RIO 6UI, 2AO, 2TR

The S-Serie E-Line RIO modules are controlled via the RS-485 serial communication protocols S-Bus and Modbus for decentralised automation using industrial quality components. The data point mix is specifically designed for building automation applications.



The compact design according to DIN 43880 enables installation in electrical distribution boxes even in the most confined spaces. Installation and maintenance are facilitated by the local manual override for each output. Remote maintenance is also possible by accessing the manual override using the Saia PCD® controller's web interface. Programming is very efficient and fast using a complete FBox library with web templates for S-Bus. Individual programs may directly access the data points via Registers and Flags. Complete documentation is included in this data sheet.

Features

- ▶ S-Bus protocol optimized for fast data exchange
- ▶ Modbus protocol for integration in multi-vendor installations*
- ▶ Local override operating level via web panel or buttons on the module
- ► Easy programming using the FBox library and web templates
- ▶ Industrial hardware in accordance with IEC EN 61131-2
- ► Pluggable terminal blocks
- ▶ Bridge connectors for power supply and communication
- ▶ Bus termination on board
- ► Configurable Bi-Colour LEDs and labelling for I/Os

General technical data

Power supply

Supply voltage	24 VDC, –15/+20% max. incl. 5% ripple (in accordance with EN/IEC 61131-2)
Power consumption	1.2 3 W
Power supply bridge	24 VDC, 5 A max., up to 40 modules

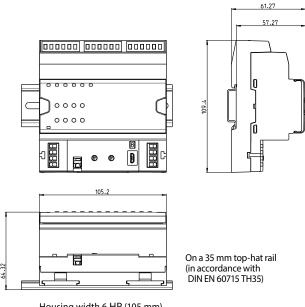
Interfaces

Communications interface	RS-485 Baud rate: 9,600, 19,200, 38,400, 57,600, 115,200 bps (Autobauding) Micro USB, Type B			
Address switch	Two rotary switches 09 Address range 098			
Bus termination	Integrated switch to activate and inactivate resistor termination			

General data

Ambient temperature	Operation: 0 +55 °C Storage: −40 +70 °C
Protection class	IP 20
Package	Single carton package with 1 Module incl. terminal blocks, 1 bridge connector

Dimensions and installation



Housing width 6 HP (105 mm) Compatible with electrical control cabinet (in accordance with DIN 43880, size 2×55 mm)

^{*} By default the module is working in S-Bus Data Mode with Autobaud detection. To configure Modbus the Windows-based application "E-LineApp" is required

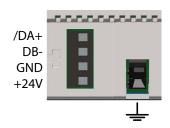
Terminal technology

Push-in spring terminals enable wiring with rigid or flexible wires with a diameter up to 1.5 mm². A max. of 1 mm² is permitted with cable end sleeves.



Connection concept

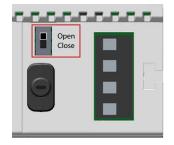
For easy installation the power supply and communication bus is available together at one connector. The push-in spring terminals enable wiring as well support the connector bridge.





Bus termination

The module provides an active bus termination. It is switched off by factory default. To enable the termination, the switch need to be in the "Close" position.



Status LED

OFF No Power

Green Communication OK
Green blink Auto bauding in progress
Orange No communication

Red Error

Red/Green alternate Booter mode

(e.g. during Firmware download)

Red blink Internal fatal error



Service interface

The USB interface provides access to the communication protocol configuration. Firmware updates can also be downloaded via Saia PG5® Firmware Download tool.



Reset button

Pushed over 20 seconds: The button needs to be pushed for minimum 20 seconds and released during the first minute after power up. All user settings are reset to factory default values.

Pushed at power up: Power off the device and press the button. Power on and release the button before 5 seconds have passed. The device stays in boot mode for further actions like firmware download etc.



Input/Output configuration

Universal Inputs

Number	6		
Electrical isolation	no		
Signal range and measured values (Configurable by FBoxes or Modbus)	$ \begin{array}{cccccccccccccccccccccccccccccccccccc$		
Maximum input voltage	+32 V		
Input filter time (DC)	Channel Update	4 ms (all channels are updated during this time)	
	Hardware input filter time Voltage measurement $\tau = 18 \text{ ms}$		
	Digital input filter 10 values		

Analogue input mode

Mode		Resolution [bit]	Resolution (measured value)	Accuracy (at TAmbient = 25°C)	Display
Voltage	010 V	12	2.7 mV RIN = 27 kΩ	0.3% of measured value ± 10 mV	0 1000 (standard) or user scaling
Resistance	02500 Ω	12	0.50 0.80 Ω Measuring current : 1.0 1.3 mA	0.3% of measured value \pm 3 Ω	0 25'000 or user scaling
Resistance	0300 kΩ	13	$0 \dots 3000 \ \Omega : \dots 1 \dots 2 \ \Omega$ $3000 \dots 7500 \ \Omega : \dots 2 \dots 4 \ \Omega$ $7.5 \dots 15 \ k\Omega : \dots 4 \dots 10 \ \Omega$ $15 \dots 40 \ k\Omega : \dots 10 \dots 40 \dots 100 \ \Omega$ $40 \dots 70 \ k\Omega : \dots 100 \dots 200 \ \Omega$ $100 \dots 300 \ k\Omega : \dots 1.5 \ k\Omega$ Measuring current: $30 \ uA \dots 1.3 \ mA$	0.3% of measured value \pm 8 Ω 0.3% of measured value \pm 15 Ω 0.3% of measured value \pm 40 Ω 0.3% of measured value \pm 160 Ω 0.5% of measured value \pm 400 Ω 1.0% of measured value \pm 800 Ω 2.5% of measured value \pm 5000 Ω	0 300'000 or user scaling
NTC10k [2]		13	-40 +120 °C : 0.05 0.1 °C	-20 +60°C: ± 0.6°C -30 +80°C: ± 1.0°C -40 +120°C: ± 2.8°C	-400 1200 [1]
NTC20k [2]		13	-10 +80 °C : 0.02 0.05 °C -20 +150 °C : < 0.15 °C	-15+75°C: ±0.6°C -20+95°C: ±1.0°C +95+120°C: ±2.5°C +120+150°C: ±5.8°C	-200 1500 [1]
Pt 1000		12	–50 +400 °C : 0.15 0.25 °C Measuring current : 1.0 1.3 mA	0.3% of measured value ± 0.5°C	-500 4000
Ni 1000		12	–50 +210 °C : 0.09 0.11 °C Measuring current : 1.0 1.3 mA	0.3% of measured value ± 0.5°C	-500 2100
Ni 1000 L&S		12	–30 +140 °C : 0.12 0.15 °C Measuring current : 1.0 1.3 mA	0.3% of measured value ± 0.5°C	-300 1400

Digital input mode

Switching level	Low: <5V High: >10V
Input current	Max. 3.5mA

Analog output

Number	2		
Resolution	10 bit		
Signal range	0 10V		
Protection	Short-circuit protection		
Resolution	9.77 mV		
Max. load at output	3.3 kΩ (3.3 mA @ 10 V)		
Accuracy (at TAMBIENT = 25°C)	0.3% of the value ± 10 mV		
Residual ripple	<15 mVpp		
Temperature error (0°C +55°C)	±0.2%		
Output delay	Channel update 1 ms (all channels are updated during this time)		
	Time constant of hardware output filter Voltage measurement $\tau = 2.5 \text{ ms}$		
Manual operation	Local override operation by buttons		

^[1] The PCD register outputs the value 0 ... 300 kΩ.
[2] Range 0 ... 300 kΩ must be used. The temperature for NTC are not standardized and may differ depending on the manufacturer.

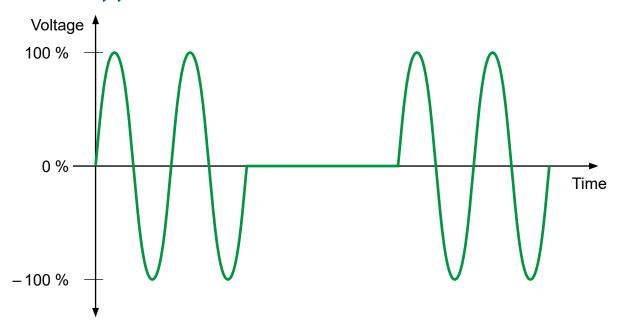
A CSV file can be used for the value generation with a linearization FBox. The CSV file can be found on the support page (link, see last page).

Input/Output configuration

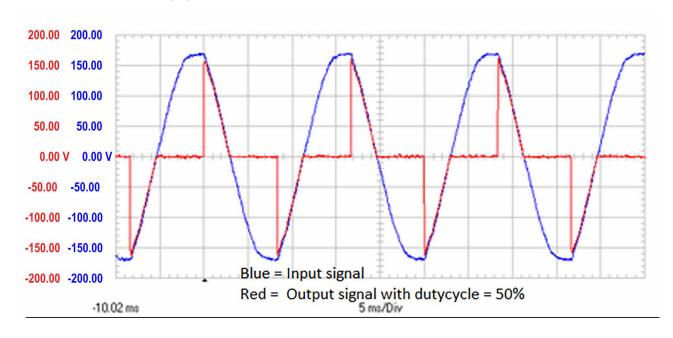
Triac output

Number	2		
Switching voltage max.	24 VAC / 230 VAC		
Switching current max	1 A (AC1) per triac, sum 1 A (AC1) if both triacs are turned on at the same time		
Mode	Phase control (1)		
	PWM (0) Time 1600 s, standard 300 s		
Manual operation	Local override operation by buttons		

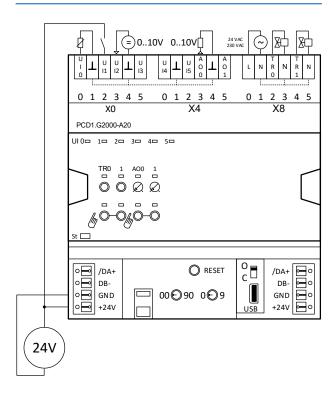
PWM of 50% duty cycle



Phase control with 50% duty cycle

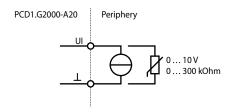


Assignment overview

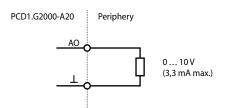


Connection diagrams

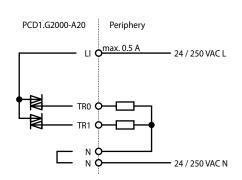
Universal inputs



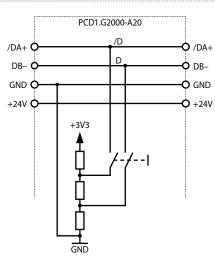
Analogue output



Triac outputs



Power supply and bus termination



LED Signalisation

Status LED

OFF No Power

Green Communication OK
Green blink Auto bauding in progress
Orange No communication

Red Error

Red/Green alternate Booter mode

(e.g. during Firmware download)

Red blink Internal fatal error

Universal inputs / outputs

I/O type	mode	OFF	GREEN	Blink (1Hz)
Analogue input	010V	0 325 mV	0.325 10 V	> 10 V
	0 2500 Ω	-	Value in range	>2k5 or open*
	0 300 kΩ	-	Value in range	>300 kΩ or open*
	Pt1000 -		Value in range	< –50°C * > 400°C or open
	Ni1000	-	Value in range	< –50°C * > 210°C or open
	Ni1000LS	-	Value in range	<-30°C * > 140°C
	Digital	<5 V	>10 V	-
Analogue output	-	0 325 mV	0.325 10 V	-
Triac output	-	0%	100%	-
* To avoid error indication (blinking LED), unused inputs should be configured in voltage range (default).				
Remarks: In case of error on analogue I/O (overflow), the LED will blink at 1 Hz.				

Manual mode

The Manual override LED is Off in automatic mode and orange in case of manual override is active.

LED colour

- ► Off (automatic)
- ► Orange manual mode active

LED blink code

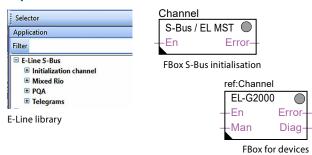
- ► No blink (local manual override)
- ▶ Blinking 1 flash per second (remote manual override)

Programming



The modules are addressed and programmed with Saia PG5® Fupla FBoxes. Web templates are available for the operation and visualisation of the manual override function.

Fupla



Communication FBox

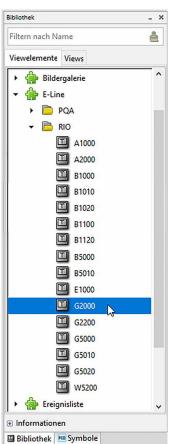
- ▶ Data exchange for I/O via optimised S-Bus
- ► Configurable save state for bus interruption or timeout
- ► Direct generation of the symbols
- ▶ Reading and writing of the status of the manual override status
- ► Direct compatibility with web macros



Further information, including which FBoxes are supported, Getting Started, etc., can be found on our support page www.sbc-support.com.

Web templates

Web templates are available for the operation and visualisation of the manual override function.







By using the local override function, commissioning can take place independently of the master station.

In addition, the manual operation can also be controlled remotely using a touch panel. If the bus line is cut off, the module keeps the manually set values. Traditional manual operation in the control cabinet door via potentiometers and switches can therefore be completely replaced by this solution.

Five operating modes can be selected for the manual operating function:

Operat-	Description		Operation		
ing modes		at the module	via remote communica- tion		
1	Manual operation deactivated	×	×		
2	Operation permitted from the module only	✓	×		
3	Operation permitted from the module and limited operation from the panel. If manual operation is activated at the module, it cannot be reset from the panel.	✓	(condi- tional)		
4	Unlimited operation from the panel and module	✓	✓		
5	Panel operation (remote)	×	✓		



Depending on the application, reset of manually set values is allowed from a panel. To address this requirement, it is possible to deactivate or limit manual operation function.

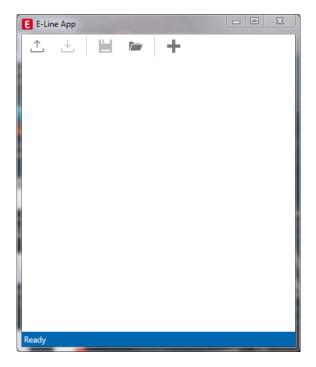


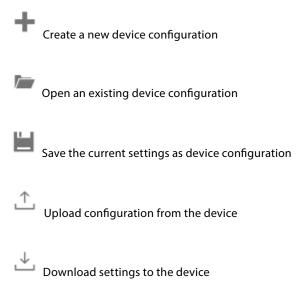
The inputs of the E-Line RIO modules can be addressed via the standard S-Bus. However the FBox from the E-Line library is used for the configuration of these modules.

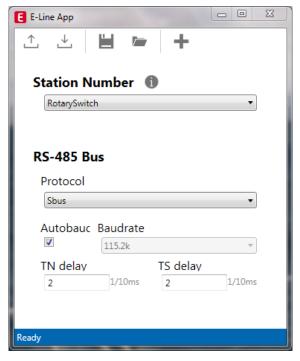
It is therefore recommended to use the optimised S-Bus protocol and the corresponding FBoxes from the E-Line library. Mixed mode operation is not recommended.

E-line App device setup

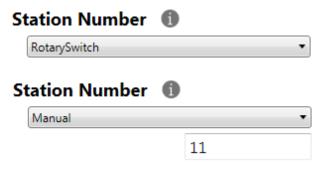
E-Line RIOs support the device setup by a windows application program connected via USB. The installer is available for download from the SBC support page: www.sbc-support.com → E-Line RIO IO Modules.







The station number can be set by the rotary switches at the device in the range of $0\dots 98$. If the rotary switches are set to position 99 the station number can be defined by the device configuration in a range of $0\dots 253$.



The serial communication protocol can be defined either as S-Bus or Modbus. By default the modules are delivered from factory with S-Bus.

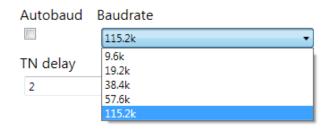
RS-485 Bus

Protocol

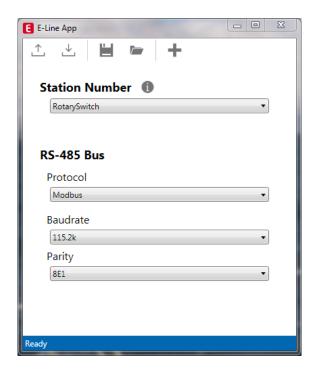


The Baudrate can be defined as automatic detection (default) or set to a specific value. The drop down choice will be available when the check box "Automatic" is unchecked. TN delay and TS delay shall be left at their default values of 2.

S-Bus settings

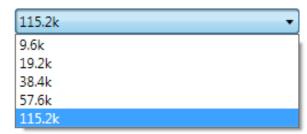


Modbus settings



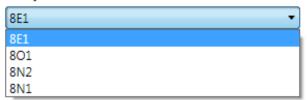
The Baudrate is set by default to 115 k. It can be defined as choice of the list.

Baudrate



For best interoperability, the Parity Mode and number of Stop Bits can also be set.

Parity



S-Bus communication

S-Bus communication is based on Saia PCD® S-Bus Data Mode. Only the set-up of a unique S-Bus address within the communication line is required to establish a communication between Saia PCD® controllers and E-Line RIO modules. The address can be set using the rotary switches at the front of the module. The baud rate will be learned from the network by factory default. In addition a Windows-based application is available for manual parameter setup. Configuration parameters as well as manual override state and value are saved non-volatile. A delay of about one second between a manual state change and non-volatile saving has to be taken into consideration.

Device address

▶ 0...98 Address is taken from the rotary switches

▶ 99 Address is taken from the device configuration. The address is settable with the E-Line configuration software.

Start-up procedure

► Reboot: All outputs are cleared (Off state)

► <1 sec. Output in manual operation are set according to the state before power down.

▶ Outputs in automatic mode

If, after reboot, no telegram is received within the "safe state power-on timeout," the module enters into the safe state mode and sets the outputs according to their configured values.

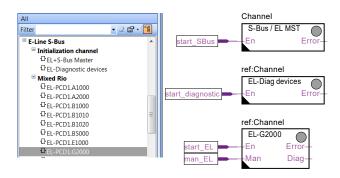
On reception of a valid command telegram the outputs are controlled by the communication. When no communication update follows within the "safe state com. timeout" the module enters into safe state and sets the outputs according to their configured values.

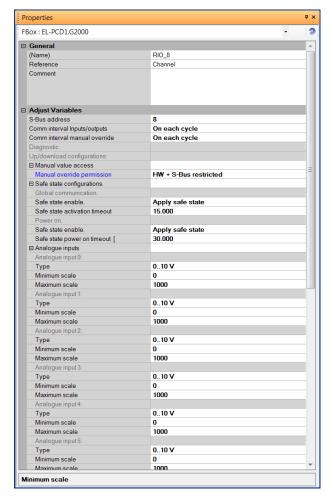
Usage of the E-Line module-specific FBoxes

The usage of the E-Line module-specific FBoxes from the E-Line S-Bus Fupla library allows an easy and efficient commissioning of the E-Line RIO.

The FBox allows the definition and configuration of all possible functionalities of the E-Line RIO like manual override permission, usage of safe state mode, behaviour and colour of the LED's and so on.

In the background, the FBox uses the fast 'E-Line S-Bus' protocol for a high speed communication between the master and the RIO.





Direct access to the RIO media with standard S-Bus send and receive telegrams

The following chapter describes the media and parameter mapping to Registers and Flags for individual programming. For efficient PCD programming the E-Line RIO FBox family and templates are suitable for most applications. Only individual programming (e.g. Instruction List) requires standard S-Bus communication.

Universal inputs

Input	Input Value	Read/Write	Mode	Range Min	Range Max	Read/Write
Analogue Input 0	Register 0	R	Register 360	Register 380	Register 400	RW
Analogue Input 1	Register 1	R	Register 361	Register 381	Register 401	RW
Analogue Input 2	Register 2	R	Register 362	Register 382	Register 402	RW
Analogue Input 3	Register 3	R	Register 363	Register 383	Register 403	RW
Analogue Input 4	Register 4	R	Register 364	Register 384	Register 404	RW
Analogue Input 5	Register 5	R	Register 365	Register 385	Register 405	RW

Mode Configuration Register:

 $0:0\dots 10 \text{ V}$ (default) Value scaled within Range Min and Range Max $3:0\dots 2500 \,\Omega$ Value scaled within Range Min and Range Max

4 : Pt1000 Value in 1/10°C (23.4 °C → 234)
5 : Ni1000 Value in 1/10°C (23.4 °C → 234)
6 : Ni1000LS Value in 1/10°C (23.4 °C → 234)

9 : Digital input Value for Input open, <5 V = 0, Input >10 V, 24 VDC = 1

Status	Status Value	Read/Write
Status AIO AI3	Register 6	R
Status Al4 Al5	Register 7	R

Register format:

1 byte for each analogue input status.

(e.g. byte 0: AI0

byte 1: Al1 byte 2: Al2

byte 3: AI3)

Bit 0 Analogue input over-range

Bit 1 Analogue input under-range

Status is cleared when the input returns to normal state

Analogue Outputs

Output	Output Value	Read/Write	Manual override Communication	Read/Write [1]	Manual override Local	Read/Write [2]
Analogue output 0	Register 50	RW	Register 90	RW	Register 94	RW
Analogue output 1	Register 51	RW	Register 91	RW	Register 95	RW

^[1] Writable only if S-Bus permission is set in the configuration, otherwise write has no effect

[2] Writing to these registers has no effect. Used only if hardware permission is set in the configuration

Normal operation: The outputs are set according the flag set by the communication.

Manual operation: The output are set according to the manual command, the communication flags are ignored.

Safe State: In case of a broken communication, a safe state value can be applied, see table Safe State Configuration.

Register format for manual override via S-Bus (Reg. 90 \dots 91):

Bit 0 Current output value

Bit 30 1: output is driven in manual override by S-Bus

Bit 31 1: output is driven in manual override by local push buttons

Register format for local manual override (Reg. 94 ... 95):

Bit 0 Current output value

Bit 31 1: output is driven in manual override by local push buttons

Output	Range Min	Range Max.	Read/Write						
Analogue output 0	Register 440	Register 460	RW						
Analogue output 1 Register 441 Register 461 RW									
Output value 010 V == Register Value Range Min Range Max									

S-Bus communication

Triac Outputs

Mode	Mode value	Read/Write		
Mode triac 0	Register 500	RW		
Mode triac 1	Register 501	RW		

Mode:

PWM 0 (default)

Phase Control

Triac Outputs

Mode	Status value	Read/Write	Manual override Communication	Read/Write [1]	Manual override Local	Read/Write [2]
Triac 0 status*	Register 30	R	Register 92	RW	Register 96	RW
Triac 1 status*	Register 31	R	Register 93	RW	Register 97	RW

^{*} Register format: 0xnnnnmmmm (Hex): Status n == 0 Triac Off, n != 0 Triac On, m = elapsed time of PWM cycle in seconds

Normal operation: The outputs are set according the flag set by the communication.

Manual operation: The output are set according to the manual command, the communication flags are ignored.

Safe State: In case of a broken communication, a safe state value can be applied, see table Safe State Configuration.

Register format for manual override via S-Bus (Reg. 92 ... 93):

Current output value

Bit 30 1: output is driven in manual override by S-Bus

Bit 31 1: output is driven in manual override by local push buttons

Register format for local manual override (Reg. 96 ... 97):

Current output value

Bit 31 1: output is driven in manual override by local push buttons

Duty cycle	Duty cycle Value	Read/Write
Triac 0 duty cycle	Register 70	RW
Triac 1 duty cycle	Register 71	RW
		_
Period	Period Value	Read/Write
Triac 0 period	Register 520	RW
Triac 1 period	Register 521	RW

Duty cycle in % with one fix decimal place $0 \dots 100\% = Register value 0 \dots 1000.$

The Triac will be activated at the beginning of a new PWM:

cycle for a time of Triac period [s] × duty cycle [%]

Phase control: The Triac will be started every half wave for a duty

cycle percentage of 10 ms.

PWM period in seconds, valid values 1 ... 600,

factory default 30 seconds.

^[1] Writable only if S-Bus permission is set in the configuration, otherwise write has no effect
[2] Writing to these registers has no effect. Used only if hardware permission is set in the configuration

S-Bus communication

Configuration for safe state and manual override

Output	Safe State Enable	Read/Write	Safe State Value	Read/Write
Analogue Output 0	Flag 300	RW	Register 420	RW
Analogue Output 1	Flag 301	RW	Register 421	RW
Triac Output 0	Flag 380	RW	Register 480*	RW
Triac Output 1	Flag 381	RW	Register 481*	RW
Communication safe state enabl	e default 0 (disabled)		Flag 400	RW
Power-On safe state enable defa	ult 0 (disabled)		Flag 401	RW
Power-On safe state timeout [ms Valid values 1000 100,000,000			Register 590	RW
Communication safe state timeo Valid values 1000 100,000,000			Register 591	RW
Manual operation mode Bit 0: Disabled Bit 1: Remote control li Bit 2: Local operation e Bit 3: Remote control u Bits can be combined to enable	nabled, default 1 nlimited**, default 0		Register 592	RW

^{*} Duty cycle in % 0..100% = Register value 0...1000

Manual operation mode:

- ▶ Disabled (0)
- ► Local operation only (4, Bit 2 set)
- ▶ Local operation enabled, remote limited (6, Bit 1 and 2 set), default
- ▶ Local and remote operation enabled (12, Bit 2 and 3 set)
- ► Remote operation only, local operation disabled (8, Bit 3 set)

The safe state enable flag and the safe state value are combined in the following way:

- Setting the enable flag to 0 keep the output value unchanged in case of safe state occurrence.
- Setting the enable flag to 1 writes the safe state value in case of safe state occurrence.

Device Information

Firmware version (Decimal xyyzz, 10802 → 1.08.02)	Register 600	R
Number of supported registers	Register 601	R
Number of supported flags	Register 602	R
Product type (ASCII String)***	Register 605 608	R
Hardware version (Hex)	Register 609	R
Serial number (Hex)	Register 611 612	R
Communication protocol (1:S-Bus Slave, 3:Modbus)	Register 620	R
Communication baud rate	Register 621	R
Communication auto baud enable (0:disabled, 1:enabled)	Register 622	R
Communication TN delay *	Register 623	R
Communication TS delay **	Register 624	R
Communication module address	Register 626	R

^{*}Time in 0.1 ms (e.g. 2 means 200 us) before setting activation of RS-485 line driver send mode (only used for S-Bus slave protocol)
**Time in 0.1 ms (e.g. 2 means 200 us) before sending the first character after line driver activation (only used for S-Bus slave protocol)
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**Time in 0.1 ms (e.g. 2 means 200 us) before senting the first character after line driver activation (only use

^{**} If manual operation is locally activated at the module, the output value and manual state cannot be set/reset remotely

Modbus communication

Modbus fulfils the requirements for standard communication protocols. It is based on Modbus RTU. The Windows-based configuration software is required to enable and set up the Modbus communication parameters. The device address can be set up with the rotary switches at the front of the module. Configuration parameters as well as manual override state and value are saved non-volatile. A delay of about one second between a manual state change and non-volatile saving has to be taken into consideration.

Device address

▶ 0...98 Address is taken from the rotary switches

▶ 99 Address is taken from the device configuration. The address is settable with the E-Line configuration software

Start-up procedure

► Reboot: All outputs are cleared (Off state)

► <1 sec. Output in manual operation are set according to the state before power down.

▶ Outputs in automatic mode

If, after reboot, no telegram is received within the "safe state power-on timeout," the module enters into the safe state mode and sets the outputs according to their configured values.

On reception of a valid command telegram the outputs are controlled by the communication. When no communication update follows within the "safe state com. timeout" the module enters into safe state and sets the outputs according to their configured values.

The following chapter describes the media and parameter mapping to Registers and Flags (=Coils).

Supported Modbus services:

- ► Function code 1 (read outputs)
- ► Function code 3 (read registers)
- ► Function code 15 (write multiple outputs)
- ► Function code 16 (write multiple registers)

Read coils

Request								
Address Function Start Address Number of coils to read CRC								
0254	1	High-Byte	Low-Byte	High-Byte	Low-Byte	High-Byte	Low-Byte	

Reply						
Address	Function	No. of Byte		Data	CF	RC
0254	1	0 256	Coil 0 7	Coil 8 15	 High-Byte	Low-Byte

Write coils

Request												
Address	Function	Start A	ddress	Number of Coils to write		Coil data			CF	RC		
0254	15	High-Byte	Low-Byte	High-Byte Low-Byte		No. of Bytes Coil 0 7 ···		•••	High-Byte	Low-Byte		

Reply										
Address	Address Function Start Address Number of written Coils CRC									
0254	15	High-Byte	Low-Byte	High-Byte	Low-Byte	High-Byte	Low-Byte			

Read register

Request							
Address Function Start Address No. of Register to read CRC							
0254	3	High-Byte	High-Byte Low-Byte		Low-Byte	High-Byte	Low-Byte

Reply							
Address	Function	No. of Byte	Register Sta	art Addr + 0	Addr + n	CF	RC
0254	3	0 256	High-Byte	Low-Byte		High-Byte	Low-Byte

Write register

Request											
Address	Function	Start A	ddress	No. of R	egisters	No. of Bytes	Data Word: S	tart Addr + 0	Addr + n	CF	RC
0254	16	High-Byte	Low-Byte	High-Byte	Low-Byte	2 256	Low-Byte	High-Byte	•••	High-Byte	Low-Byte

	Reply							
	Address	ss Function Start Address No of written Registers		CF	RC			
Ī	0254	16	High-Byte	Low-Byte	High-Byte	Low-Byte	High-Byte	Low-Byte

The CRC has to be calculated over all telegram bytes starting with address field up to the last data byte. The CRC has to be attached to the data. Please find an example at the appendix of this document. For more details, please refer the publicly available Modbus documentation www.modbus.org.

Modbus communication

Universal inputs

Input	Input Value	Read/Write	Mode	Range Min	Range Max	Read/Write
Analogue Input 0	Register 0-1	R	Register 720-721	Register 760-761	Register 800-801	RW
Analogue Input 1	Register 2-3	R	Register 722-723	Register 762-763	Register 802-803	RW
Analogue Input 2	Register 4-5	R	Register 724-725	Register 764-765	Register 804-805	RW
Analogue Input 3	Register 6-7	R	Register 726-727	Register 766-767	Register 806-807	RW
Analogue Input 4	Register 8-9	R	Register 728-729	Register 768-769	Register 808-809	RW
Analogue Input 5	Register 10-11	R	Register 730-731	Register 770-771	Register 810-811	RW

Mode Configuration Register:

0:0...10 V (default) Value scaled within Range Min and Range Max $3:0...2500 \Omega$ Value scaled within Range Min and Range Max

4 : Pt1000 Value in 1/10°C (23.4 °C → 234) 5 : Ni1000 Value in 1/10°C (23.4 °C → 234) 6 : Ni1000LS Value in 1/10°C (23.4 °C → 234)

8:0...300 kΩ Value scaled within Range Min and Range Max

9 : Digital input Value for Input open, <5 V = 0, Input >10 V, 24 VDC = 1

Status	Status Value	Read/Write
Status AIO AI3	Register 12-13	R
Status Al4 Al5	Register 14-15	R

Register format:

1 byte for each analogue input status.

(e.g. byte 0: AI0 byte 1: AI1

byte 2: Al2 byte 3: Al3)

Bit 0 Analogue input over-range Bit 1 Analogue input under-range

Status is cleared when the input has again a correct value.

Analogue Outputs

Output	Output Value	Read/Write	Manual override Communication	Read/Write [1]	Manual override Local	Read/Write [2]
Analogue output 0	Register 100-101	RW	Value Reg. 180 Enable Reg. 181	RW	Value Reg. 188 Enable Reg. 189	RW
Analogue output 1	Register 102-103	RW	Value Reg. 182 Enable Reg. 183	RW	Value Reg. 190 Enable Reg. 191	RW

^[1] Writable only if S-Bus permission is set in the configuration, otherwise write has no effect

 $\label{eq:Normal operation: The outputs are set according the flag set by the communication.$

Manual operation: The output are set according to the manual command, the communication flags are ignored.

Safe State: In case of a broken communication, a safe state value can be applied, see table Safe State Configuration.

Register format for manual override via Modbus (Reg. 180 ... 183):

Bit 0 Current output value

Enable Reg. Bit 14 1: output is driven in manual override by Modbus

Enable Reg. Bit 15 1: output is driven in manual override by local push buttons

Register format for local manual override (Reg. 188 ... 191):

Value Reg. Bit 0 Current output value

Enable Reg. Bit 15 1: output is driven in manual override by local push buttons

Output	Range Min	Range Max.	Read/Write			
Analogue output 0	Register 880-881	Register 920-921	RW			
Analogue output 1	Register 882-883	Register 922-923	RW			
Output value 010 V == Register Value Range Min Range Max						

^[2] Writing to these registers has no effect. Used only if hardware permission is set in the configuration

Modbus communication

Triac Outputs

Mode	Mode value	Read/Write
Mode triac 0	Register 1000-1001	RW
Mode triac 1	Register 1002-1003	RW

Mode: PWM

0 (default)

Phase Control 1

Triac Outputs

Mode	Status value	Read/Write	Manual override Communication	Read/Write [1]	Manual override Local	Read/Write [2]
Triac 0 status*	Register 60-61	RW	Value Reg. 184 Enable Reg. 185	RW	Value Reg. 192 Enable Reg. 193	RW
Triac 1 status*	Register 62-63	RW	Value Reg. 186 Enable Reg. 187	RW	Value Reg. 194 Enable Reg. 195	RW

^{*} Register format 30-31, 32-33: 0xnnnnmmmm (Hex): Status n == 0 Triac Off, n != 0 Triac On, m = elapsed time of PWM cycle in seconds

Normal operation: The outputs are set according the flag set by the communication.

Manual operation: The output are set according to the manual command, the communication flags are ignored.

Safe State: In case of a broken communication, a safe state value can be applied, see table Safe State Configuration.

Register format for manual override via Modbus (Reg. 184 ... 187):

Bit 0 Current output value

Enable Reg. Bit 14 1: output is driven in manual override by Modbus

Enable Reg. Bit 15 1: output is driven in manual override by local push buttons

Register format for local manual override (Reg. 192 ... 195):

Value Reg. Bit 0 Current output value

Enable Reg. Bit 15 1: output is driven in manual override by local push buttons

Duty cycle	Duty cycle Value	Read/Write
Triac 0 duty cycle	Register 140-141	RW
Triac 1 duty cycle	Register 142-143	RW
Period	Period Value	Read/Write
Triac 0 period	Register 1040-1041	RW
Triac 1 period	Register 1042-1043	RW

Duty cycle in % with one fix decimal place $0 \dots 100\% = \text{Register value } 0 \dots 1000.$

PWM: The Triac will be activated at the beginning of a new

cycle for a time of Triac period [s] × duty cycle [%]

Phase control: The Triac will be started every half wave for a duty

cycle percentage of 10 ms.

PWM period in seconds, valid values 1 \dots 600,

factory default 30 seconds.

^[1] Writable only if S-Bus permission is set in the configuration, otherwise write has no effect

^[2] Writing to these registers has no effect. Used only if hardware permission is set in the configuration

Configuration for safe state and manual override

Output	Safe State Enable	Read/Write	Safe State Value	Read/Write
Analogue Output 0	Flag 300	RW	Register 840-841	RW
Analogue Output 1	Flag 301	RW	Register 842-843	RW
Triac Output 0	Flag 380	RW	Register 960-961*	RW
Triac Output 1	Flag 381	RW	Register 962-963*	RW
Communication safe state enable	Flag 400	RW		
Power-On safe state enable defa	Flag 401	RW		
Power-On safe state timeout [ms Valid values 1000 100,000,000			Reg. 1180, 1181	RW
Communication safe state timeo Valid values 1000 100,000,000			Reg. 1182, 1183	RW
Manual operation mode Bit 0: Disabled Bit 1: Remote control li Bit 2: Local operation e Bit 3: Remote control u Bits can be combined to enable	nabled, default 1 nlimited*, default 0		Register 1184	RW

^{*} Duty cycle in % 0..100% = Register value 0...1000

Manual operation mode:

- ▶ Disabled (0)
- ► Local operation only (4, Bit 2 set)
- ▶ Local operation enabled, remote limited (6, Bit 1 and 2 set), default
- ▶ Local and remote operation enabled (12, Bit 2 and 3 set)
- ► Remote operation only, local operation disabled (8, Bit 3 set)

The safe state enable flag and the safe state value are combined in the following way:

- Setting the enable flag to 0 keep the output value unchanged in case of safe state occurrence.
- Setting the enable flag to 1 writes the safe state value in case of safe state occurrence.

Device Information

Firmware version (Decimal xyyzz, 10802 → 1.08.02)	Register 1200	R
Number of supported registers	Register 1202	R
Number of supported flags	Register 1204	R
Product type (ASCII String)*3	Register 1210 1217	R
Hardware version (Hex)	Register 1218	R
Serial number (Hex)	Register 1222 1224	R
Communication protocol (1: S-Bus Slave, 3: Modbus)	Register 1240	R
Communication baud rate	Register 1242	R
Communication auto baud enable (0:disabled, 1:enabled)	Register 1244	R
Communication Mode	Register 1250	R
0: 8,E,1; 1: 8,O,1; 2: 8,N,2; 3: 8,N,1		
Communication module address	Register 1252	R

^{*1} The eight registers contain the ASCII characters of the product type. E.g. for PCD1.A2000-A20: 1210...1217: 5043H | 4431H | 2E41H | 3230H | 3030H | 2D41H | 3230H | 0000H

^{**} If manual operation is locally activated at the module, the output value and manual state cannot be set/reset remotely

CRC Generation Example

(Source: http://modbus.org/docs/PI_MBUS_300.pdf, the following content of this page is copied from the referenced document. In case of any questions, please check out the original source)

The function takes two arguments: unsigned char *puchMsg; A pointer to the message buffer containing binary data to be used for generating the CRC unsigned short usDataLen; The quantity of bytes in the message buffer. The function returns the CRC as a type unsigned short.

CRC Generation Function

```
unsigned short CRC16(puchMsg, usDataLen);
unsigned char *puchMsg :
                                                                                           /* message to calculate CRC upon */
                                                                                          /* quantity of bytes in message */
unsigned short usDataLen;
{
            unsigned char uchCRCHi = 0xFF;
                                                                                          /* high byte of CRC initialized */
                                                                                          /* low byte of CRC initialized */
            unsigned char uchCRCLo = 0xFF;
            unsigned uIndex ;
                                                                                          /* will index into CRC lookup table */
            while (usDataLen--)
                                                                                          /* pass through message buffer */
                         uIndex = uchCRCHi ^ *puchMsgg++;
                                                                                          /* calculate the CRC */
                         uchCRCHi = uchCRCLo ^ auchCRCHi[uIndex];
                         uchCRCLo = auchCRCLo[uIndex];
             return (uchCRCHi << 8 | uchCRCLo);
High-Order Byte Table
/* Table of CRC values for high-order byte */
static unsigned char auchCRCHi[] = {
0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40,
0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41,
0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41,
0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40,
0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0xC1, 0x81, 0x40, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41,
 0 \times 00, \ 0 \times C1, \ 0 \times 81, \ 0 \times 40, \ 0 \times 01, \ 0 \times C0, \ 0 \times 80, \ 0 \times 41, \ 0 \times C0, \ 0 \times 80, \ 0 \times 41, \ 0 \times 00, \ 0 \times C1, \ 0 \times 81, \ 0 \times 40, \ 0 \times C1, \ 0 \times
0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40,
0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41,
0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41,
0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40,
0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40,
0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41,
0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40,
0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41,
0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40, 0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41,
0x00, 0xC1, 0x81, 0x40, 0x01, 0xC0, 0x80, 0x41, 0x01, 0xC0, 0x80, 0x41, 0x00, 0xC1, 0x81, 0x40 );
Low-Order Byte Table
/* Table of CRC values for low-order byte */
static char auchCRCLo[] = {
0x00, 0xC0, 0xC1, 0x01, 0xC3, 0x03, 0x02, 0xC2, 0xC6, 0x06, 0x07, 0xC7, 0x05, 0xC5, 0xC4, 0x04,
0xCC, 0x0C, 0x0D, 0xCD, 0x0F, 0xCF, 0xCE, 0x0E, 0x0A, 0xCA, 0xCB, 0x0B, 0xCB, 0x0B, 0xCB,
0x08, 0x18, 0x19, 0x09, 0x18, 0x08, 0x0A, 0x1A, 0x1E, 0x0E, 0x0F, 0x1F, 0x0D, 0x1D, 0x1C, 0x0C,
0x14, 0xD4, 0xD5, 0x15, 0xD7, 0x15, 0xD6, 0xD6, 0xD2, 0x12, 0x13, 0xD3, 0x11, 0xD1, 0xD0, 0x10,
0xF0, 0x30, 0x31, 0xF1, 0x33, 0xF3, 0xF2, 0x32, 0x36, 0xF6, 0xF7, 0x37, 0xF5, 0x35, 0x34, 0xF4,
0x3C, 0xFC, 0xFD, 0x3D, 0xFF, 0x3F, 0x3E, 0xFE, 0xFA, 0x3A, 0x3B, 0xFB, 0x3B, 0xFB, 0x3B,
0x28, 0xE8, 0xE9, 0x29, 0xEB, 0x2B, 0x2A, 0xEA, 0xEE, 0x2E, 0x2F, 0xEF, 0x2D, 0xED, 0xEC, 0x2C,
0xE4, 0x24, 0x25, 0xE5, 0x27, 0xE7, 0xE6, 0x26, 0x22, 0xE2, 0xE3, 0x23, 0xE1, 0x21, 0x20, 0xE0,
0xA0, 0x60, 0x61, 0xA1, 0x63, 0xA3, 0xA2, 0x62, 0x66, 0xA6, 0xA7, 0x67, 0xA5, 0x65, 0x64, 0xA4,
0x6C, 0xAC, 0xAD, 0x6D, 0xAF, 0x6F, 0x6E, 0xAE, 0xAA, 0x6A, 0x6B, 0xAB, 0x69, 0xAB, 0x68,
0x78, 0x88, 0x89, 0x79, 0x88, 0x78, 0x78, 0x88, 0x8E, 0x7E, 0x8F, 0x7E, 0x8D, 0x8C, 0x7C,
0x84, 0x74, 0x75, 0x85, 0x77, 0x87, 0x86, 0x76, 0x72, 0x82, 0x83, 0x73, 0x81, 0x71, 0x70, 0x80,
0x50, 0x90, 0x91, 0x51, 0x93, 0x53, 0x52, 0x92, 0x96, 0x56, 0x57, 0x97, 0x55, 0x95, 0x94, 0x54,
0x9C, 0x5C, 0x5D, 0x9D, 0x5F, 0x9F, 0x9E, 0x5E, 0x5A, 0x9A, 0x9B, 0x5B, 0x9A, 0x9B, 0x5B, 0x5B, 0x5B, 0x5B, 0x9B,
0x88, 0x48, 0x49, 0x89, 0x4B, 0x8B, 0x8A, 0x4A, 0x4E, 0x8E, 0x8F, 0x4F, 0x8D, 0x4D, 0x4C, 0x8C,
0x44, 0x84, 0x85, 0x45, 0x87, 0x47, 0x46, 0x86, 0x82, 0x42, 0x43, 0x83, 0x41, 0x81, 0x80, 0x40);
```



NOTE

Extra low voltages (ELV) or secure low voltages (SELV) are voltages up to 50 Volts.



NOTE

Low voltages are voltages between 50 ... 250 Volts.

INSTALLATION DIRECTION FOR SWITCHING LOWER VOLTAGES



For reasons of safety it is not allowed that extra low voltages and low voltages are connected to two adjacent triac outputs. Neither may different phases may be connected to two adjacent triac outputs.

If a Saia PCD® system module is connected to low voltage, then all components which are electrically connected to this system must be approved for low voltage.

When using low voltage, all connections to the triac outputs, which are connected to the same circuit, must be protected by a common fuse.

The individual load circuits, on the other hand, may be protected individually by a fuse.



ATTENTION

These devices must only be installed by a professional electrician, otherwise there is the risk of fire or the risk of an electric shock.



WARNING

Product is not intended to be used in safety critical applications, using it in safety critical applications is unsafe.



WARNING - Safety

The unit is not suitable for the explosion-proof areas and the areas of use excluded in EN 61010 Part 1.



WARNING - Safety

Check compliance with nominal voltage before commissioning the device (see type label). Check that connection cables are free from damage and that, when wiring up the device, they are not connected to voltage.



NOTE

In order to avoid moisture in the device due to condensate build-up, acclimatise the device at room temperature for about half an hour before connecting.



CLEANING

The device can be cleaned in dead state with a dry cloth or cloth soaked in soap solution. Do not use caustic or solvent-containing substances for cleaning.



MAINTENANCE

These devices are maintenance-free. If damaged during transportation or storage, no repairs should be undertaken by the user.



GUARANTEE

Opening the module invalidates the guarantee.



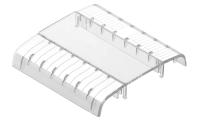
WEEE Directive 2012/19/EC Waste Electrical and Electronic Equipment directive

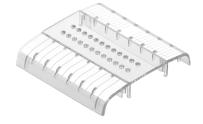
The product should not be disposed of with other household waste. Check for the nearest authorized collection centers or authorized recyclers. The correct disposal of end-of-life equipment will help prevent potential negative consequences for the environment and human health.



EAC Mark of Conformity for Machinery Exports to Russia, Kazakhstan or Belarus.







PCD1.G2000-A20

PCD1.K0206-025



Terminal set 32304321-003-S

Order details

Туре	Short description	Description	Weight
PCD1.G2000-A20	E-Line S-Serie RIO 6UI, 2AO, 2TR	E-Line S-Serie combined input/output module manual override operating level for all outputs status LED for inputs and outputs supply 24 VDC 6 universal digital/analogue inputs - Digital inputs 24 VDC - analoque inputs 12 bits - 010 V, Pt/Ni 1000, Ni 1000 L&S, NTC, - 02500 Ohm, 07500 Ohm, 0300 kOhm 2 analogue outputs 10 bits, 010 V 2 triac outputs 24 VAC/1 A or 230 VAC/1 A 1 interface RS-485 (S-Bus and Modbus) 1 USB Service interface	200 g
PCD1.K0206-005	E-Line labelling set 5 × 6 HP*	E-Line cover and labelling set consisting of 5 × covers (6 HP = 105 mm) and labelling sheet for mounting in the automation control cabinet	365 g
PCD1.K0206-025	E-Line labelling set 5 × 6 HP* with holes	E-Line cover and labelling set with holes consisting of 5 × covers (6 HP = 105 mm) with holes for manual override operating level and labelling sheet for mounting in the automation control cabinet	365 g
32304321-003-S	Terminal set	6-pin terminal. Set of 6 terminal blocks	40 g

^{*} Horizontal pitch: 1 HP corresponds to 17.5 mm

Saia-Burgess Controls AG

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